

TITLE

Subclass	SSUE CLASSIFICATION
Class	SSUE CLAS



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U.S. UTILITY Patent Application

O.O. O I	ILIT I ator	Application	
Or	O.I.P.E.	PATENT DATE	
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/964211	DF	324	2	2858	11. 12. 14

Akihiko Ito Yoshihito Kobayashi Yoshiyuki Masuo Tsuyoshi Yamashita

Semiconductor device testing apparatus and a test tray for use in the testing apparatus ${\cal C}_{\rm c}$

PTO-2040 12/99

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TERMINAL		DRAWINGS		CLAIM	CLAIMS ALLOWED			
□ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.			
☐ The term of this patent				NOTICE OF ALL	OWANCE MAILED			
subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)					
The term of this patent shall								
not extend beyond the expiration date of U.S Patent. No.				ISS	UE FEE			
				Amount Due	Date Paid			
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this patent have been disclaimed.	(Legal Instrume	ents Examiner)	(Date)					
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The information disclosed herein may be res Possession outside the U.S. Patent & Trader					5, Sections 122, 181 and 368.			
								

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